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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

2-25-05

Stylesheet Version v18.0

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| Title of Invention | Distributing Fault Indications And Maintaining And Using A Data Structure Indicating Faults To Route Traffic In A Packet Switching System |
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Application Number : 09/519282



Confirmation Number: 9726

First Named Applicant: Daniel Lenoski

Attorney Docket Number: 97437

Art Unit: 2661

Examiner: Ian N Moore

Search string: (6674721 or 6747972 or 6788689 or 6816492 or 6826186 or 6735173 or 6654342
or 6728211 or 5475682 or 5533020 or 5844890 or 5768572).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
|---------|----------|------------|------------|------------------|------|-------|----------|
| 9/11/05 | 1 | 6674721 | 2004-01-06 | Dittia et al. | | | |
| | 2 | 6747972 | 2004-06-08 | Lenoski et al. | | | |
| | 3 | 6788689 | 2004-09-07 | Turner et al. | | | |
| | 4 | 6816492 | 2004-11-09 | Turner et al. | | | |
| | 5 | 6826186 | 2004-11-30 | Dittia et al. | | | |
| | 6 | 6735173 | 2004-05-11 | Lenoski et al. | | | |
| | 7 | 6654342 | 2003-11-25 | Dittia et al. | | | |
| | 8 | 6728211 | 2004-04-27 | Peris et al. | | | |
| | 9 | 5475682 | 1995-12-12 | Choudhury et al. | | | |
| | 10 | 5533020 | 1996-07-02 | Byrn et al. | | | |
| | 11 | 5844890 | 1998-12-01 | Delp et al. | | | |
| | 12 | 5768572 | 1998-06-16 | George et al. | | | |

Signature

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| Examiner Name | Date |
| <i>Ian N Moore</i> | <i>7/1/05</i> |

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|---|---|----------------------|----------------|---------------------|-------|
| FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE MODIFIED PATENT AND TRADEMARK OFFICE | | Application Number | 09/519,282 | | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | Filing Date | March 7, 2000 | | |
| | | First Named Inventor | Daniel Lenoski | | |
| | | Group Art Unit | 2661 | | |
| | | Examiner Name | Ian N Moore | | |
| Sheet | 1 | of | 1 | Attorney Docket No. | 97437 |

| PENDING U.S. PATENT APPLICATIONS | | | | |
|----------------------------------|----------|--------------------|----------------------------|-------------------|
| EXAMINER'S INITIALS | Cite No. | Application Number | Filing Date (MM-DD-YYY) | Name of Applicant |
| 9NM | AA | 09/705,450 | 11-02-2000 | Turner et al. |
| ↓ | AB | 09/784,787 | 02-15-2001 | Turner et al. |
| ↓ | AC | 09/705,395 | 11-02-2000 | Turner et al. |
| ↓ | AD | 09/949,125 | 09-07-2001 | Dejanovic et al. |

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|--------------------|--------------------|-----------------|--------|
| Examiner Signature | <i>Ian N Moore</i> | Date Considered | 7/1/05 |
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Application Number : 09/519282 

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Art Unit: 2661

Examiner: Ian N. Moore

Search string: (6654342 or 6674721 or 6728211 or 6735173 or 6747972 or 6788689 or 6826186),pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
|------|----------|------------|------------|----------------|------|-------|----------|
| 9nm | 1 | 6654342 | 2003-11-25 | Dittia et al. | | | |
| | 2 | 6674721 | 2004-01-06 | Dittia et al. | | | |
| | 3 | 6728211 | 2004-04-27 | Peris et al. | | | |
| | 4 | 6735173 | 2004-05-11 | Lenoski et al. | | | |
| | 5 | 6747972 | 2004-06-08 | Lenoski et al. | | | |
| | 6 | 6788689 | 2004-09-07 | Turner et al. | | | |
| ✓ | 7 | 6826186 | 2004-11-30 | Dittia et al. | | | |

Signature

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|-----------------|---------------|
| Examiner Name | Date |
| <i>9m N mel</i> | <i>7/1/05</i> |

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|--|---|--|---|----------------------|------------------------|
| FORM PTO-1449 MODIFIED | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | Application Number | 09/519,282 |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | | Filing Date | March 7, 2000 |
| | | | | First Named Inventor | Daniel Lenoski |
| | | | | Group Art Unit | 2661 |
| | | | | Examiner Name | RAO, SEEMA SRINIVAS |
| Sheet | 1 | of | 1 | Attorney Docket No. | 97437 |

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| JNM | AQ | JONATHAN S. TURNER, "An Optimal Nonblocking Multicast Virtual Circuit Switch," June 1994, Proceedings of Infocom, 8 pages. |
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| JAT | AS | TURNER ET AL., "System Architecture Document for Gigabit Switching Technology," Aug. 27, 1998, Ver. 3.5, ARL-94-11, Washington University, St. Louis, MO, 110 pages. |

JNM 8/29/2005

JNM mmr

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First Named Applicant: Daniel Lenoski

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| | 2 | 6674721 | 2004-01-06 | Dittia et al. | | | |
| | 3 | 6728211 | 2004-04-27 | Peris et al. | | | |
| | 4 | 6735173 | 2004-05-11 | Lenoski et al. | | | |
| | 5 | 6747972 | 2004-06-08 | Lenoski et al. | | | |
| | 6 | 6788689 | 2004-09-07 | Turner et al. | | | |
| | 7 | 6826186 | 2004-11-30 | Dittia et al. | | | |

Signature

| Examiner Name | Date |
|-------------------|---------------|
| <i>Im N Moore</i> | <i>6/4/05</i> |

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|---|---|----------------------|----------------|
| FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE MODIFIED PATENT AND TRADEMARK OFFICE | | Application Number | 09/519,292 |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | Filing Date | March 7, 2000 |
| | | First Named Inventor | Daniel Lenoski |
| | | Group Art Unit | 2661 |
| | | Examiner Name | Ian N Moore |
| Sheet | 1 | of | 1 |
| | | Attorney Docket No. | 97437 |

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| EXAMINER'S INITIALS | Cite No. | Application Number | Filing Date (MM-DD-YYY) | Name of Applicant |
| g n m | AA | 09/705,450 | 11-02-2000 | Turner et al. |
| J | AB | 09/784,787 | 02-15-2001 | Turner et al. |
| | AC | 09/705,395 | 11-02-2000 | Turner et al. |
| Y | AD | 09/949,125 | 09-07-2001 | Dejanovic et al. |

| | | | |
|-----------------------|--------------------|--------------------|---------------|
| Examiner Signature | <i>Ian N Moore</i> | Date Considered | <i>6/4/05</i> |
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